

Ejercicio 8b:

```
b) xorn R0, R1, R2:  
  inv.in = R1  
  INV.not  
  alu.in = R0  
  ALU.xor  
  R2 := alu.out
```

Ejercicio 8c:

```
c) Fetch:  
  MAR = PC  
  MEM.READ  
  IRL = MDR  
  IR0 = MDR  
  INC.IN = PC  
  INC.4  
  PC = INC.OUT  
  endif  
  if IR0[3] = 1:  
    MAR = PC
```